

CLAIM

I claim:

1. An automatic radio frequency signal switching circuit, comprising:

a set of multiple AV signal input terminals coupled to an analog type electronic switch; an analog /digital adapter of an integrated circuit equipped with multiple converters each of which is coupled respectively to an output terminal of each set of analog video signal so as to permit analog video signals to be transformed into digital signals;

an output of a central processor is coupled to said analog type electronic switch, a radio frequency modulator, an RF switch, ANT/CATV RF switch and LED indicator; wherein multiple set of AV input terminals having their video signals transformed into high voltage signals and transmitted to an input terminal of said central processor; said central processor provides a voltage signal to said analog type electronic switch, RF modulator and RF switch so as to select AV signals in priority to deliver via said analog type electronic switch and said RF modulator and further via said RF switch to a television; at the same time said central processor provides a voltage signal to shut off ANT/CATV RF switch so as to permit RF signals are transmitted to said TV without interference

with an LED indicating AV equipment being in operation;

When no AV signals are input, said central processor instantly outputs another set of high level voltage signals to said ANT/CATV RF amplifying circuit and said RF switch so as to transmit ANT/CATV signals to a television, effecting automatic RF signal switching operation and an LED indicator shows that said ANT/CATV is in operation.

2. The automatic radio frequency signal switching circuit as claimed in claim 1 wherein said analog/digital adapter is an integrated circuit equipped with multiple converters, permitting input video signals to be transformed into digital ones; as an output terminal of one of said converters produces a high level electric voltage, indicating that AV signals are being delivered to said analog/digital adapter. a piece of said AV equipment is being activated by a user, at then said high level electric voltage signal is transmitted to an input terminal of said central processor that can judge which piece of AV equipment is activated from the corresponding input terminals thereof receiving said signal, and memorizes sequential orders of the activation of said AV equipment in a memory unit of said central processor and detects as well as leaves alone signal input terminals receiving no signals and only takes care of the part receiving input signals.